| Search Notes | | | |
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| Application/Control No. | Applicant(s)/Patent under Reexamination |
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| 10/773,050 | SALAM ET AL. |
| Examiner | Art Unit |
| Peter Coughlan | 2129 |

| | SEAR | CHED | |
|-------|----------|-----------|----------|
| Class | Subclass | . Date | Examiner |
| 706 | 1 | 2/21/2007 | PDC |
| 706 | · 15 | 2/21/2007 | PDC |
| 706 | 45 | 2/21/2007 | PDC |
| 706 | 16 | 2/21/2007 | PDC |
| 600 | 586 | 2/21/2007 | PDC |
| 318 | 254 | 2/21/2007 | PDC |
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| TNI | INTERFERENCE SEARCHED | | |
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| SEARCH NOT (INCLUDING SEARCH | |) |
|---|-----------|------|
| | DATE | EXMR |
| Eastcapacitor, quadrant multipliers, transconductance, amplifiers, load resistance, d flip flops, neural networks, Waheed, | 2/21/2007 | PDC |
| EastIIFathi, Erten, feed forward, VLSI, medium, transmission, communication, databus, weights, training, memory, storage, | 2/21/2007 | PDC |
| East-III-pattern comparison, analog, digital, continious time, substrate, interconnection, one dimension, pulsed NN | 2/21/2007 | PDC |
| IEEE-Fathi M. Salam, Khurram Waheed, VLSI, capacitor, quadrant multiplier, d flip flops, bi stable multivibrator, substrate, | 2/21/2007 | PDC |
| Inventors Fathi M. Salam Khurram Waheed | 2/21/2007 | PDC |
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